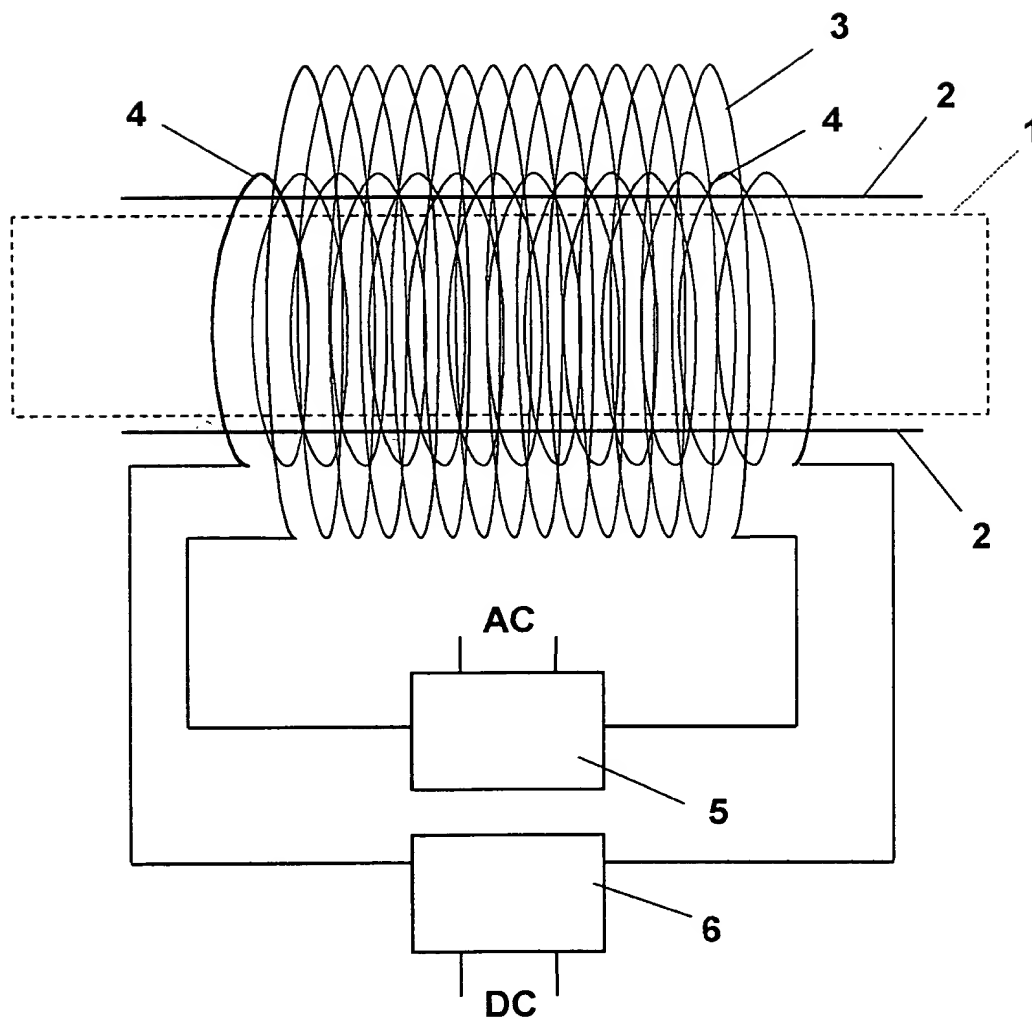
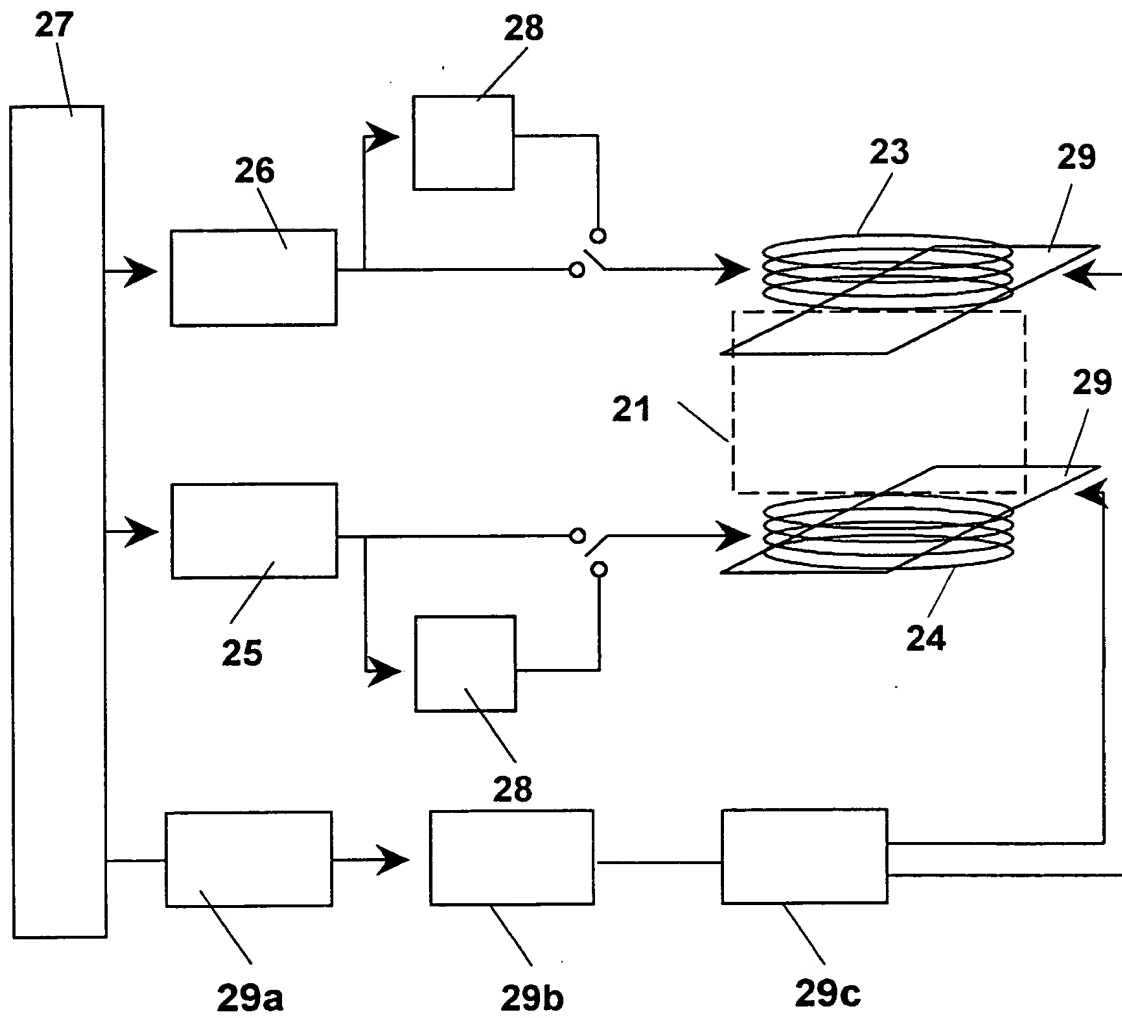


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Fig. 1

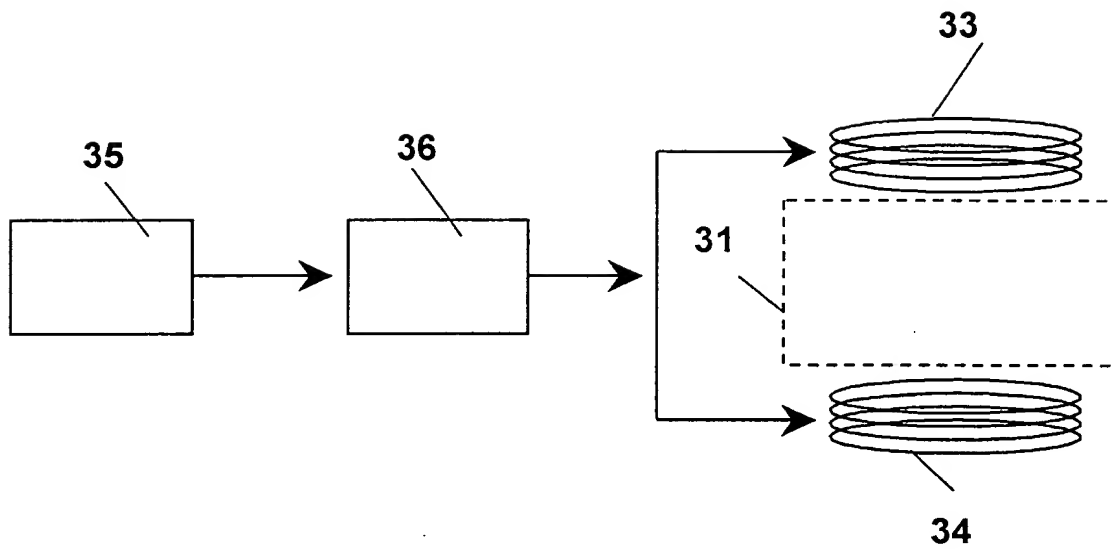
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Fig. 2

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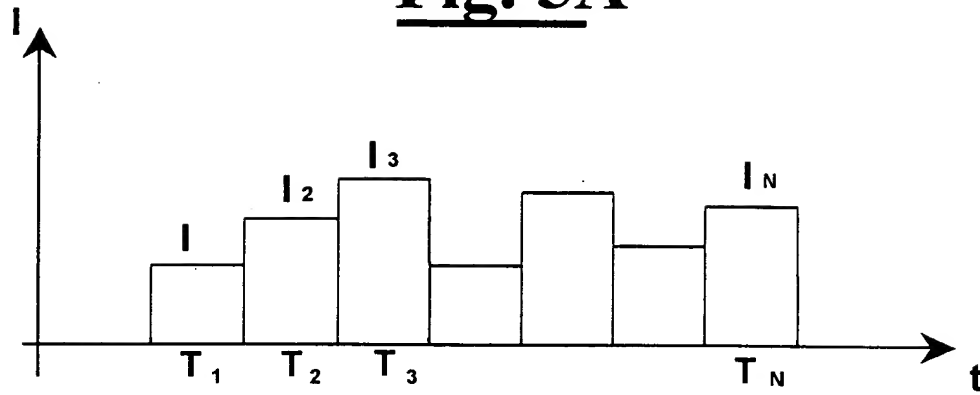
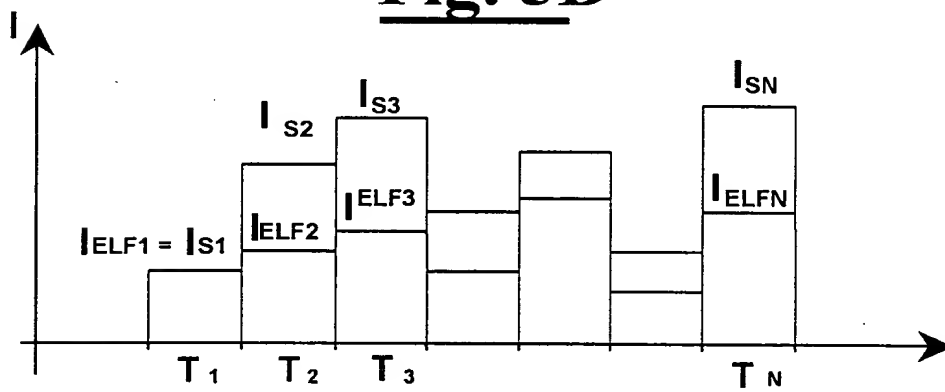
Fig. 3

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Fig. 1 is a block diagram of a system for processing data from a computer 49. The computer 49 is connected to a unit 45, which in turn connects to a unit 46. Unit 46 is connected to a unit 47, which then connects to a unit 48. Unit 48 is connected to a unit 43, which is connected to a unit 44. Unit 44 is connected to a unit 41, which is connected to a unit 42. Unit 42 is connected to a unit 40.

Fig. 4

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Fig. 5AFig. 5BFig. 5C